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Inventor : Masahiko NAKAYAMA
Title : CONTROL OF AMPLITUDE LEVEL OF BASEBAND SIGNAL TO BE
TRANSMITTED ON THE BASIS OF THE NUMBER OF TRANSMISSION CODES
Assignee : NEC Corporation

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

- 48 Pages of Specification including Abstract and Claims
- 17 Numbered Claims Calculated as 17 Claims for Fee Purposes
- 11 Sheets of Drawing Containing Figures 1 to 11.
- X Declaration and Power of Attorney
- X Priority is Claimed under 35 U.S.C. §119:
- Convention Date December 17, 1998 for Japan Appln. S.N. 10-359586
- X Certified Priority Application
- X PTO-1449 with 2 references
- X Assignment
- X Return-Addressed Post Card

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**CONTROL OF AMPLITUDE LEVEL OF BASEBAND SIGNAL
TO BE TRANSMITTED ON THE BASIS OF THE
NUMBER OF TRANSMISSION CODES**

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BACKGROUND OF THE INVENTION

1. Field of the Invention:

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The present invention relates generally to a baseband
signal multiplexing circuit provided in a transmitter using
a spread spectrum communication system for multiplexing a
10 plurality of baseband signals spread with different spread
codes, and more particularly to a method of controlling a
transmission level of a code-multiplexed baseband signal.

2. Description of the Related Art:

In recent years, attention is focused on a spread
15 spectrum communication system resistant to interference and
jamming as a communication system for use in a mobile
communication system. In the spread spectrum communication
system, a transmission side first spreads a digital signal
such as voice and image data using a spread code such as a
20 pseudorandom noise code to convert it into a baseband
signal with a broader bandwidth than that of the original
digital signal. The baseband signal is modulated with
modulating systems such as PSK (Phase Shift Keying), FSK
(Frequency Shift Keying) or the like to form a high
25 frequency signal which is then transmitted.

One of the spread spectrum communication systems is a DS (Direct Sequence) system. Spread codes used in the system are allocated for respective communication channels with cross-correlation thereof being set to be sufficiently little. Signals spread with different spread codes appear as broadband noises when despread is performed, and only the target signal can be acquired by selecting a spread code used in despread on a reception side. For this reason, the same frequency band can be used by a plurality of communication channels to perform CDMA (Code Division Multiple Access).

According to the spread spectrum communication system, it is possible not only to perform multiple access with a plurality of transmitters, but also to multiplex a plurality of baseband signals spread with different spread codes on one transmitter for transmission. Such a system for multiplexing a plurality of baseband signals using a plurality of spread codes for transmission is called a multicode system.

Fig. 1 shows a configuration of a conventional transmitter using the spread spectrum communication system of such a multicode system when it is simply formed as an extension of prior arts.

The conventional transmitter using the spread spectrum communication system comprises signal processor 1,

baseband signal multiplexing circuit 82, RF (Radio Frequency) transmitter 10, and antenna 11.

Signal processor 1 converts digitized transmission data such as voice and image into transmission frame
5 signals in accordance with the spread spectrum communication system and spreads the transmission data with different spread codes to produce a plurality of baseband signals.

Baseband signal multiplexing circuit 82 comprises
10 baseband filters 301 to 303, adder 701, and D/A converter 9.

Baseband filters 301 to 303 are formed from digital FIR (Finite Impulse Response) filters or the like and limit bands of the respective baseband signals outputted from signal processor 1. Adder 701 adds the baseband signals
15 outputted from respective baseband filters 301 to 303 to produce one baseband signal with code-multiplexing.

D/A converter 9 converts the baseband signal which is a digital signal produced by adder 701 into an analog signal. RF transmitter 10 converts the analog signal
20 converted by D/A converter 9 into a signal at a radio frequency for transmission through antenna 11 as a radio wave.

However, in the aforementioned case where multiple codes are transmitted by multiplexing a plurality of
25 baseband signals spread with different spread codes on one

transmitter for transmission, the need arises for adjusting the transmission level of respective codes.

For example, assuming that a signal power for one code transmission is P over the full dynamic range of D/A converter 9 for effective utilization thereof, a signal level for multiple-code transmission of N multiplexed codes is provided at a signal power obtained by $N \times P$ when the circuit provides an output with simple addition. As a result, a signal including signal power information exceeds the bit width of D/A converter 9 to cause an overflow. This means that D/A converter 9 should be set to have a wider dynamic range. When N transmission codes are multiplexed with effective utilization of the dynamic range of D/A converter 9 in one code transmission, it is required that the amplitude of an signal applied to D/A converter 9 is adjusted to $1/\sqrt{N}$ over the square root of N . The amplitude may be adjusted to $1/\sqrt{N}$ over the square root of N to obtain the power of $1/N$ since the power is proportional to the square of the amplitude.

However, the number of transmission codes is not always constant but changed depending on a type of data to be transmitted (voice, image or the like) or the amount of data. For this reason, adjustment of the amplitude of a signal applied to D/A converter 9 to $1/\sqrt{N}$ over the square root of N at all times regardless of the number of multiplexed

codes causes a problem that the dynamic range is not fully used and a relative quantization error is increased to deteriorate transmission quality. In this manner, a conventional transmitter using the spread spectrum communication system can not effectively use a dynamic range of a D/A converter when the number of transmission codes is changed.

SUMMARY OF THE INVENTION

10 It is an object of the present invention to provide a baseband signal multiplexing circuit capable of effectively using a dynamic range of a D/A converter independently of the number of transmission codes even when the number of transmission codes is changed.

15 To achieve the aforementioned object, a baseband signal multiplexing circuit according to the present invention has a plurality of baseband filters, adding means, level adjusting means, D/A converting means, and gain setting means.

20 The level adjusting means adjusts an amplitude value of a baseband signal produced by the adding means based on a control signal and then outputs the signal. The gain setting means calculates a gain set value with which the amplitude value of the baseband signal outputted from the
25 level adjusting means is adjusted to an amplitude value

matching the dynamic range of the D/A converting means
based on the number of transmission codes which is the
number of multiplexed baseband signals, and notifies the
level adjusting means of the gain set value with the
5 control signal.

Since the present invention uses the gain setting
means to control the amplitude level of the baseband signal
to be transmitted based on the number of transmission codes,
the baseband signal applied to the D/A converting means is
10 always adjusted to match the dynamic range of the D/A
converting means regardless of the number of transmission
codes, thereby making it possible to effectively utilize
the dynamic range of the D/A converting means.

Another baseband signal multiplexing circuit
15 according to the present invention comprises a plurality of
baseband filters, adding means, a plurality of level
adjusting means, D/A converting means, and gain setting
means.

The plurality of level adjusting means respectively
20 adjust amplitude values of a plurality of baseband signals
with bands limited by the respective baseband filters based
on a control signal and then output the signals.

In another baseband signal multiplexing circuit
according to the present invention, gain setting means
25 calculates, for respective level adjusting circuits, gain

set values with which an amplitude value of a baseband
signal outputted from adding means is adjusted to match a
dynamic range of D/A converting means based on the number
of transmission codes which is the number of multiplexed
5 baseband signals and based on interchannel ratio
information for specifying an amplitude ratio of respective
baseband signals when a plurality of baseband signals are
multiplexed, notifies the level adjusting means of the
respective gain set values with a plurality of control
10 signals.

The present invention can perform relative level
adjustment for each code since level adjusting means are
provided for respective baseband signals spread with
respective transmission codes.

15 A yet further baseband signal multiplexing circuit
according to the present invention comprises adding means,
a baseband filter, level adjusting means, D/A converting
means, and gain setting means.

The adding means adds and code-multiplexes respective
20 baseband signals input thereto to produce one baseband
signal. The baseband filter limits the band of the
baseband signal produced by the adding means.

Since the present invention provides the baseband
filter after the code-multiplexing with the adding means,
25 only one baseband filter is required.

A still further baseband signal multiplexing circuit according to the present invention comprises a plurality of baseband filters, a plurality of first level adjusting means, adding means, a second level adjusting means, and
5 D/A converting means.

The plurality of first level adjusting means respectively adjust amplitude values of respective baseband signals input thereto based on a plurality of first control signals and then output the signals. The adding means adds
10 and code-multiplexes the plurality of baseband signals outputted from the respective first level adjusting means to produce on baseband signal. The second level adjusting means adjusts the amplitude of the baseband signal produced by the adding means based on a second control signal and
15 then outputs the signal.

Gain setting means outputs the first control signals for adjusting an amplitude ratio of the respective baseband signals in accordance with interchannel ratio information for specifying an amplitude ratio of respective baseband
20 signals when a plurality of baseband signals are multiplexed. The gain setting means also calculates a gain set value with which the amplitude value of the baseband signal outputted from the second level adjusting means is adjusted an amplitude value matching the dynamic range of
25 the D/A converting means based on the number of

transmission codes which is the number of multiplexed baseband signals, and notifies the second level adjusting means of the gain set value with the second control signal.

A further baseband signal multiplexing circuit
5 according to the present invention comprises a plurality of first level adjusting means, adding means, a baseband filter, second level adjusting means, and D/A converting means.

The first level adjusting means respectively adjust
10 amplitude values of baseband signals input thereto based on a plurality of first control signals. The adding means adds and code-multiplexes the plurality of baseband signals outputted from the respective first level adjusting means to produce one baseband signal and then outputs the signal.
15 The baseband filter limits the band of the baseband signal produced by the adding means. The second level adjusting means adjusts the amplitude value of the baseband signal with the band limited by the baseband filter based on the second control signal and then outputs the signal.

20 The present invention uses the first level adjusting means to adjust an amplitude ratio of the respective baseband signals and uses the second level adjusting means to provide overall amplitude level adjustment, thereby making it possible to facilitate fine adjustment of an
25 amplitude level.

The level adjustment circuit according to the present invention comprises a plurality of bit shift means, a plurality of switches, and adding means.

5 The plurality of bit shift means produce signals by shifting baseband signals input thereto to the right by different certain bits. The plurality of switches select outputs from the respective bit shift means in accordance with a desired gain to be set. The adding means adds outputs from the respective switches for output as one
10 signal.

The present invention can provide a reduction in power consumption since a level adjusting circuit is formed without using a multiplier.

A yet further baseband signal multiplexing circuit
15 according to the present invention comprises a plurality of baseband filters, adding means, gain setting means, and D/A converting means.

The plurality of baseband filters limit bands of respective baseband signals input thereto and adjust
20 amplitude values of the baseband signals based on control signals, and then outputs the signal.

According to an embodiment of the present invention, each of the baseband filters comprises a plurality of delay elements, a plurality of coefficient multipliers, and an
25 adder.

invention;

Fig. 3 is a block diagram showing a configuration of level adjusting circuit 8 in Fig. 2;

Fig. 4a is a diagram showing values in binary number representation corresponding to decimal places;

Fig 4b is a diagram showing $1/2^{1/2}$ represented in a binary number;

Fig. 4c is a diagram showing $1/3^{1/2}$ represented in a binary number;

Fig. 5 is a block diagram showing a configuration of baseband signal multiplexing circuit 22 in a transmitter using the spread spectrum communication system according to a second embodiment of the present invention;

Fig. 6 is a block diagram showing a configuration of baseband signal multiplexing circuit 32 in a transmitter using the spread spectrum communication system according to a third embodiment of the present invention;

Fig. 7 is a block diagram showing a configuration of baseband signal multiplexing circuit 42 in a transmitter using the spread spectrum communication system according to a fourth embodiment of the present invention;

Fig. 8 is a block diagram showing a configuration of baseband signal multiplexing circuit 52 in a transmitter using the spread spectrum communication system according to a fifth embodiment of the present invention;

Fig. 9 is a block diagram showing a configuration of baseband signal multiplexing circuit 62 in a transmitter using the spread spectrum communication system according to a sixth embodiment of the present invention;

5 Fig. 10 is a block diagram showing a configuration of baseband signal multiplexing circuit 72 in a transmitter using the spread spectrum communication system according to a seventh embodiment of the present invention; and

10 Fig. 11 is a block diagram showing a configuration of baseband filters 331, 332 and 333 in Fig. 10.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

First Embodiment

Referring now to Fig. 2, there is shown a transmitter
15 using the spread spectrum communication system according to a first embodiment of the present invention for illustrating an example of a configuration capable of multiplexing up to three transmission codes. The number of transmission codes refers to the number of actually
20 multiplexed baseband signals and is three at maximum in the baseband signal multiplexing circuit of the embodiment.

The transmitter using the spread spectrum communication system in the embodiment comprises signal processor circuit 1, baseband signal multiplexing circuit 2,
25 RF transmitter 10, and antenna 11.

Baseband signal multiplexing circuit 2 differs from
baseband signal multiplexing circuit 82 in the conventional
transmitter using the spread spectrum communication system
shown in Fig. 1 in that level adjusting circuit 8 is newly
5 provided between adder 701 and D/A converter 9 and that
gain setting circuit 6 is newly provided.

Signal processor 1 in the embodiment outputs
transmission code number information 104 to baseband signal
multiplexing circuit 2. On the basis of transmission code
10 number information 104 transmitted from signal processor 1,
gain setting circuit 6 calculates a gain set value with
which an amplitude value of a baseband signal outputted
from level adjusting circuit 8 is adjusted to an amplitude
value matching the dynamic range of D/A converter 9, and
15 notifies level adjusting circuit 8 of the gain set value
through control signal 101.

Level adjusting circuit 8 adjusts an amplitude value
of a signal multiplexed by adder 701 based on control
signal 101 outputted from gain setting circuit 6 and then
20 outputs the signal to D/A converter 9. While level
adjusting circuit 8 is generally formed using a multiplier,
a conventional level adjusting circuit formed using a
multiplier has had a problem of high power consumption.
For this reason, in the embodiment, level adjusting circuit
25 8 is formed without using a multiplier to reduce a scale of

a circuit and to reduce power consumption.

Fig. 3 is a block diagram showing a configuration of level adjusting circuit 8 in Fig. 2.

Level adjusting circuit 8 comprises bit shift
5 sections 12 to 16, switches 17 to 21, and an adder 711.

Bit shift sections 12 to 16 shift the input baseband signals to the right by 0, 1, 3, 4, and 6 bits before output. Specifically, each of bit shift sections 12 to 16 produces a signal shifted by some bits depending on which
10 bit in the input signal is used as the top. Bit shift sections 12 to 16 may be realized without forming each of them with a shift register or the like.

Switches 17 to 21 are controlled with control signal 101 and select the outputs from bit shift sections 12 to 16
15 for output to adder 711. Adder 711 adds the output from switches 17 to 21 to provide one signal which is outputted to D/A converter 9.

In the case of a small device with a relatively small number of transmission codes such as a mobile phone, a
20 scale of a circuit can be small if a level adjusting circuit is realized with a configuration for shifting baseband signals to be transmitted by fixed bits and then combining and adding the signals as in the embodiment.

Next, the operation of level adjusting circuit 8
25 shown in Fig. 3 will be described. The input baseband

signal in Fig. 3 is divided into individual signals shifted to the right by 1, 3, 4 and 6 bits, respectively. "S" in Fig. 3 represents a sign bit.

An amplitude value of a digitized baseband signal is represented in a binary number. In the binary number, an increase in digit by one represents a double magnitude, while a reduction in digit by one represents a half magnitude. Thus, when a number smaller than one is represented in a binary number, respective decimal places take values shown in Fig. 4. In an amplitude level represented in a binary number, a shift of n bits to the right results in a value of $1/2^n$.

For example, 1 over the square root of 2 ($1/2^{1/2}$) is "0.10110100" in binary number representation as shown in Fig. 4b. Similarly, 1 over the square root of 3 ($1/3^{1/2}$) is "0.10010100" in binary number representation as shown in Fig. 4c.

Therefore, level adjusting circuit 8 may add signals shifted by 1, 3, 4 and 6 bits at adder 711 in order to adjust the amplitude of the input baseband signal to 1 over the square root of 2 ($1/2^{1/2}$) in the case of two multiplexed codes. Level adjusting circuit 8 may add signals shifted by 1, 4 and 6 bits at adder 711 in order to adjust the amplitude of the input baseband signal to 1 over the square root of 3 ($1/3^{1/2}$) in the case of three

multiplexed codes.

Table 1 shows amounts of gain which can be set with a structure such as level adjusting circuit 8.

(Table 1)

5 Table 1 uses a case where seven bit shift sections are provided. An ideal dB value is a desired gain to be set and a true value is a value representing the ideal dB value with a ratio. An approximate dB value is a gain actually set with the level adjusting circuit.

10 For example, when a desired gain to be set is 5dB (1.77827941), an approximate dB value 5.014497547 dB can be realized.

As the number of bit shift sections is increased, an error between an ideal dB value and an approximate dB value
15 is reduced. However, even with seven bit shift sections, the error is so small as shown in Table 1 that it causes no practical problems.

Next, the operation in the embodiment will be described in detail with reference to the drawings.

20 First, description will be made for the overall operation of the transmitter using the spread spectrum communication system of the embodiment.

Binary baseband signals spread with respective spread codes at signal processor 1 are applied to baseband filters
25 301, 302 and 303 which limit the bands thereof. The

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filtered baseband signals including multilevel amplitude information are multiplexed at adder 701 and then applied to level adjusting circuit 8. Signal processor 1 notifies current transmission code number information 104 to gain setting circuit 6 which generates control signal 101 for controlling level adjusting circuit 8 such that the baseband signal outputted from level adjusting circuit 8 has an appropriate signal amplitude. D/A converter 9 converts the baseband signals with adjusted amplitude from digital to analog form. The analog signal is applied to RF transmitter 10 and transmitted from antenna 11 as a radio wave after conversion into a radio frequency. Since RF transmitter 10 does not relate directly to the present invention, the detailed configuration thereof is omitted.

Next, description will be made for detailed operations when one code is transmitted with the transmitter using the spread spectrum communication system in the embodiment.

In a state where one code transmission is performed, one of baseband filters 301, 302 and 303 is normally used. The outputs from the remaining two baseband filters are not added at adder 701, and only the signal passing through a baseband filter corresponding to one code is applied to level adjusting circuit 8.

At this point, since gain setting circuit 6 receives

setting information for one code transmission from signal processor 1 through transmission code information 104, it turns on only switch 17 in level adjusting circuit 8 shown in Fig. 3 with control signal 101 and turns off other
5 switches 18 to 21. This causes level adjusting circuit 8 to output the input baseband signal to D/A converter 9 without any processing.

Next, description will be made for detailed operations when three multiplexed codes are transmitted
10 with the transmitter using the spread spectrum communication system in the embodiment.

When three multiplexed codes are transmitted, gain setting circuit 6 turns on only switches 18, 20 and 21 by changing control signal 101. Therefore, the amplitude
15 value of the baseband signal outputted from adder 711 is a value obtained by multiplying the input signals by $1/2$, $1/16$ and $1/64$, respectively, and then adding the multiplied signals, and the obtained value is approximately equal to 1 over the square root of 3 of the amplitude value of the
20 input signal.

Similarly, when two multiplexed codes are transmitted, gain setting circuit 6 turns on all of switches 18, 19, 20 and 21 by changing control signal 101. Therefore, the amplitude value of the baseband signal outputted from adder
25 711 is a value obtained by multiplying the input signals by

1/2, 1/8, 1/16 and 1/64, respectively, and then adding the multiplied signals, and the obtained value is approximately equal to 1 over the square root of 2 of the amplitude value of the input signal.

5 In the transmitter using the spread spectrum communication system in the embodiment, level adjusting circuit 8 is provided in baseband signal multiplexing circuit 2 for adjusting the signal level based on the number of transmission codes, thereby allowing a fixed
10 signal power of an input to D/A converter 9 when the number of transmission codes is changed. Therefore, it is possible to effectively use the dynamic range of D/A converter 9 regardless of the number of transmission codes.
Second Embodiment

15 Next, description will be made for a transmitter using the spread spectrum communication system according to a second embodiment of the present invention with reference to Fig. 5. In Fig. 5, reference numerals identical to those in Fig. 2 designate the same components.

20 The embodiment differs from the first embodiment shown in Fig. 2 in that baseband signal multiplexing circuit 22 replaces baseband signal multiplexing circuit 2.

 Baseband signal multiplexing circuit 22 differs from the baseband signal multiplexing circuit 2 in that level
25 adjusting circuit 8 provided between adder 701 and D/A

converter 9 in circuit 2 is removed and level adjusting circuits 811 to 813 are newly provided between baseband filters 301 to 303 and adder 701, respectively.

Level adjusting circuits 811 to 813 have similar configurations to that of level adjusting circuit 8 shown in Fig. 2 and respectively adjust the amplitude values of baseband signals with limited bands outputted from baseband filters 301 to 303 based on control signal 101 outputted from gain setting circuit 6 and then output the signals to adder 701.

The embodiment has similar effects to those of the first embodiment as described above since the transmission level is adjusted on the basis of the number of transmission codes.

15 Third Embodiment

Next, description will be made for a transmitter using the spread spectrum communication system according to a third embodiment of the present invention with reference to Fig. 6. In Fig. 6, reference numerals identical to those in Fig. 3 designate the same components.

The embodiment differs from the second embodiment shown in Fig. 5 in that baseband signal multiplexing circuit 32 replaces baseband signal multiplexing circuit 22.

Baseband signal multiplexing circuit 32 has a similar configuration to that of baseband signal multiplexing

circuit 22 except that gain setting circuit 36 replaces gain setting circuit 6.

Also, in the embodiment, interchannel ratio information 105 is applied to gain setting circuit 36 for specifying the ratio of amplitudes of respective baseband signals when a plurality of baseband signals are multiplexed.

Gain setting circuit 36 calculates gain set values with which the amplitude value of a baseband signal outputted from adder 701 is adjusted to an amplitude value matching the dynamic range of D/A converter 9 based on transmission code number information 104 and interchannel ratio information 105 for respective level adjusting circuits 811 to 813, and notifies level adjusting circuits 811 to 813 of the respective gain set values with control signals 102₁ to 102₃.

Level adjusting circuits 811 to 813 in the embodiment independently adjust amplitude values of baseband signals with limited bands outputted from baseband filters 301 to 303 based on control signals 102₁ to 102₃ outputted from gain setting circuit 36, and then output the signals to adder 701.

The embodiment has similar effects to those of the first and second embodiments as described above since the transmission level is adjusted on the basis of the number

of transmission codes. In the embodiment, however, since level adjusting circuits 811 to 813 are provided for respective baseband signals spread with respective spread codes and control signals 102₁ to 102₃ are individually
5 provided for controlling the gain set values therefor, the embodiment provides not only level adjustment after signals are added to produce one baseband signal as in the second embodiment, but also the effect of allowing relative level adjustment for respective codes.

10 Fourth Embodiment

Next, description will be made for a transmitter using the spread spectrum communication system according to a fourth embodiment of the present invention with reference to Fig. 7. In Fig. 7, reference numerals identical to
15 those in Fig. 2 designate the same components.

The embodiment differs from the first embodiment shown in Fig. 2 in that baseband signal multiplexing circuit 42 replaces baseband signal multiplexing circuit 2.

Baseband signal multiplexing circuit 42 differs from
20 baseband signal multiplexing circuit 2 in that the position at which adder 701 is replaced with the position at which baseband filters 301 to 303 are provided, and that one baseband filter 321 replaces three baseband filters 301 to 303.

25 Adder 701 in the embodiment adds a plurality of

baseband signals outputted from signal processor 1 to produce one baseband signal.

Baseband filter 321 has a similar configuration to those of baseband filters 301 to 303 and limits the band of
5 the baseband signal outputted from adder 701.

In the embodiment, adder 701 multiplexes three codes before baseband filter 321 limits the band, thereby requiring only one baseband filter as compared with three required in the first embodiment.

10 Fifth Embodiment

Next, description will be made for a transmitter using the spread spectrum communication system according to a fifth embodiment of the present invention with reference to Fig. 8. In Fig. 8, reference numerals identical to
15 those in Figs. 2 and 6 designate the same components.

The embodiment differs from the third embodiment shown in Fig. 6 in that baseband signal multiplexing circuit 52 replaces baseband signal multiplexing circuit 32.

Baseband signal multiplexing circuit 52 differs from
20 baseband signal multiplexing circuit 32 in that level adjusting circuit 8 is provided between adder 701 and D/A converter 9 and that gain setting circuit 56 replaces gain setting circuit 36.

Gain setting circuit 56 outputs control signals 102₁
25 to 102₃ to level adjusting circuits 811 to 813 for

adjusting the amplitude ratio of respective baseband
signals in accordance with interchannel ratio information
105 transmitted from signal processor 1. Gain setting
circuit 56 also calculates a gain set value with which an
5 amplitude value of a baseband signal outputted from level
adjusting circuit 8 is adjusted to an amplitude value
matching the dynamic range of D/A converter 9 based on
transmission code number information 104 and notifies level
adjusting circuit 8 of the gain set value with control
10 signal 101.

In the embodiment, since level adjusting circuits 811
to 813 can adjust the ratio of levels among respective
baseband signals and level adjusting circuit 8 can provide
the overall level adjustment, it is possible to easily
15 provide fine level adjustment. For example, level
adjusting circuits 811 to 813 makes gain set value of 1:2:1
for respective baseband signals and level adjusting circuit
8 makes gain set value of $1/4^{1/2}$, thereby making it
possible to obtain the ratio 1:2:1 of respective baseband
20 signals and to match the level of a combined baseband
signal with the dynamic range of D/A converter 9.

Sixth Embodiment

Next, description will be made for a transmitter
using the spread spectrum communication system according to
25 a sixth embodiment of the present invention with reference

to Fig. 9. In Fig. 9, reference numerals identical to those in Figs. 7 and 8 designate the same components.

The embodiment differs from the fifth embodiment shown in Fig. 7 in that baseband signal multiplexing
5 circuit 62 replaces baseband signal multiplexing circuit 52.

Baseband signal multiplexing circuit 62 differs from baseband signal multiplexing circuit 52 in that three baseband filters 301 to 303 are removed and that baseband filter 321 is newly provided between adder 701 and level
10 adjusting circuit 8.

The embodiment requires only one baseband filter as compared with three required in the fifth embodiment.

Seventh Embodiment

Next, description will be made for a transmitter
15 using the spread spectrum communication system according to a seventh embodiment of the present invention with reference to Fig. 10. In Fig. 10, reference numerals identical to those in Fig. 2 designate the same components.

The embodiment differs from the first embodiment
20 shown in Fig. 2 in that baseband signal multiplexing circuit 72 replaces baseband signal multiplexing circuit 2.

Baseband signal multiplexing circuit 72 comprises baseband filters 331 to 333, adder 701, D/A converter 9, and gain setting circuit 76.

25 In the first to sixth embodiments, level adjusting

circuit 8 or 811 to 813 are inserted before or after adder 701 to perform signal level adjustment. In the present embodiment, however, signal level adjustment is performed by changing filter coefficients of baseband filters 331 to 333.

Gain setting circuit 76 calculates gain set values with which an amplitude value obtained by adding baseband signals outputted from baseband signals 331 to 333 is adjusted to an amplitude value matching the dynamic range of /A converter 9 based on the number of transmission codes transmitted from signal processor 1, and notifies baseband filters 331 to 333 of the gain set values with control signals 103₁ to 103₃.

Signal level adjustment is performed by inputting baseband signals to be transmitted for respective codes and control signals 103₁ to 103₃ from gain setting circuit 76 to baseband filters 331 to 333. Signals outputted from respective baseband filters 331 to 333 are code-multiplexed at adder 701 and then outputted in analog form from D/A converter 9.

Fig. 11 shows an example in which baseband filter 331 in Fig. 10 is formed from a digital FIR filter for adjusting its coefficient. Since baseband filters 332 and 333 have similar configurations to that of baseband filter 331, the description thereof is omitted.

Baseband filter 331 comprises delay elements 401₁ to 401_n, coefficient multipliers 501₀ to 501_n, and adder 751.

Delay elements 401₁ to 401_n delay input signals by a unit time and then outputs them as tap outputs.

- 5 Each of coefficient multipliers 501₀ to 501_n has a plurality of filter coefficients and multiplies each of the tap outputs delayed by delay elements 401₁ to 401_n by a filter coefficient specified by control signal 103₁.

- 10 For example, 501₀ has three filter coefficients of h_0 , $h_0/2^{1/2}$, $h_0/3^{1/2}$, and selects one of the three filter coefficients specified by control signal 103₁ for multiplication by the tap output.

- 15 Adder 751 adds $n+1$ output signals from coefficient multipliers 501₀ to 501_n and outputs the added signal to adder 701.

- 20 The input signals are delayed by delay elements 401₁ to 401_n and outputted as tap outputs. When a gain set value is one, for example, the respective tap outputs are multiplied by filter coefficients h_0, h_1, \dots, h_n at coefficient multipliers 501₀ to 501_n. The multiplication results are added at adder 751 and outputted as an output signal.

- 25 Input control signal 103₁ adjusts the filter coefficients depending on the number of transmission codes to perform control based on the number of transmission

codes.

For example, when the number of transmission codes is three, $(h_0/3^{1/2}, h_1/3^{1/2}, \dots, h_n/3^{1/2})$ are selected, and when the number of transmission codes is two, $(h_0/2^{1/2},$
5 $h_1/2^{1/2}, \dots, h_n/2^{1/2})$ are selected.

Assuming that the amplitude value of the output signal from adder 751 is y_n , respective tap outputs are x_n, x_{n-1}, \dots, x_0 , then y_n is derived from the following equation:

10
$$y_n = h_0x_n + h_1x_{n-1} + h_2x_{n-2} + \dots + h_nx_0$$

The embodiment has no need to provide a level adjusting circuit since baseband filters 331 to 333 provide signal level adjustment as well as band limitation.

In the embodiment, gain setting circuit 76 outputs
15 three control signals 103₁ to 103₃ to independently make gain set value of baseband filters 331 to 333. However, gain setting circuit 76 may output one control signal to simultaneously make gain set value for baseband filters 331 to 333. Such a configuration requires only one control
20 signal.

While the aforementioned first to seventh embodiments use the case of three transmission codes for description, the present invention is not limited thereto and similarly applicable to the case of four or more transmission codes.

25 While preferred embodiments of the present invention

have been described using specific terms, such description is for illustrative purposes only, and it is to be understood that changes and variations may be made without departing from the spirit or scope of the following claims.

Patent Office

Table 1

	MSB								LSB	
shift amount	0	1	2	3	4	5	6	7		
ideal value (dB)	1	0.5	0.25	0.125	0.0625	0.03125	0.015625	0.0078125	approximate value (dB)	
5 1.77827941	1	1	1	0	0	1	0	0	5.014497547	
4 1.584893192	1	1	0	0	1	1	0	0	4.048403956	
3 1.412537545	1	0	1	1	0	1	0	0	2.961250709	
2 1.258925412	1	0	1	0	0	0	1	0	2.046100898	
1 1.122018454	1	0	0	1	0	0	0	0	1.023050449	
0	1	0	0	0	0	0	0	0	0	
-1 0.891250938	0	1	1	1	0	0	1	0	-1.006102366	
-2 0.794328235	0	1	1	0	0	1	1	0	-1.972195958	
-3 0.707945784	0	1	0	1	1	0	1	0	-3.059349204	
-4 0.630957344	0	1	0	1	0	0	0	1	-3.974499015	
-5 0.562341325	0	1	0	0	1	0	0	0	-4.997549464	
-6 0.501187234	0	1	0	0	0	0	0	0	-6.020599913	

What is claimed is:

1. A baseband signal multiplexing circuit for multiplexing a plurality of baseband signals spread with different spread codes into one baseband signal, comprising:

5 a plurality of baseband filters for respectively limiting bands of the respective baseband signals input thereto;

adding means for adding and code-multiplexing the plurality of baseband signals with the bands limited by
10 said respective baseband filters to produce one baseband signal;

level adjusting means for adjusting an amplitude value of the baseband signal produced by said adding means based on a control signal to output the signal;

15 D/A converting means for converting the baseband signal which is a digital signal outputted from said level adjusting means into an analog signal; and

gain setting means for calculating a gain set value with which the amplitude value of the baseband signal
20 outputted from said level adjusting means is adjusted to an amplitude value matching a dynamic range of said D/A converting means based on the number of transmission codes which is the number of multiplexed baseband signals, and for notifying said level adjusting means of the gain set

25 value with said control signal.

2. A baseband signal multiplexing circuit for multiplexing a plurality of baseband signals spread with different spread codes into one baseband signal, comprising:

5 a plurality of baseband filters for respectively limiting bands of the respective baseband signals input thereto;

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a plurality of level adjusting means for respectively adjusting amplitude values of the plurality of
10 baseband signals with the bands limited by said respective baseband filters based on a control signal to output the signals;

adding means for adding and code-multiplexing the plurality of baseband signals outputted from said
15 respective level adjusting means to produce one baseband signal;

D/A converting means for converting the baseband signal which is a digital signal outputted from said adding means into an analog signal; and

20 gain setting means for calculating a gain set value with which an amplitude value of the baseband signal outputted from said adding means is adjusted to an amplitude value matching a dynamic range of said D/A

converting means based on the number of transmission codes
25 which is the number of multiplexed baseband signals, and
for notifying said level adjusting means of the gain set
value with said control signal.

3. A baseband signal multiplexing circuit for
multiplexing a plurality of baseband signals spread with
different spread codes into one baseband signal,
comprising:

5 a plurality of baseband filters for respectively
limiting bands of the respective baseband signals input
thereto;

a plurality of level adjusting means for
respectively adjusting amplitude values of the plurality of
10 baseband signals with the bands limited by said respective
baseband filters based on a plurality of control signals to
output the signals;

adding means for adding and code-multiplexing
the plurality of baseband signals outputted from said
15 respective level adjusting means to produce one baseband
signal;

D/A converting means for converting the baseband
signal which is a digital signal outputted from said adding
means into an analog signal; and

20 gain setting means for calculating, for

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25
30
respective said level adjusting circuits, gain set value
with which an amplitude value of the baseband signal
outputted from said adding means is adjusted to an
amplitude value matching a dynamic range of said D/A
converting means based on the number of transmission codes
which is the number of multiplexed baseband signals and
based on interchannel ratio information for specifying an
amplitude ratio of the respective baseband signals when the
plurality of baseband signals are multiplexed, and for
notifying said level adjusting means of the gain set values
with said plurality of control signals.

4. A baseband signal multiplexing circuit for
multiplexing a plurality of baseband signals spread with
different spread codes into one baseband signal,
comprising:

- 5
10
adding means for adding and code-multiplexing
the respective baseband signals input thereto to produce
one baseband signal;
a baseband filter for limiting a band of the
baseband signal produced by said adding means;
a level adjusting means for adjusting an
amplitude value of the baseband signal with the band
limited by said baseband filter based on a control signal
to output the signal;

15 D/A converting means for converting the baseband
signal which is a digital signal outputted from said level
adjusting means into an analog signal; and

gain setting means for calculating a gain set
value with which the amplitude value of the baseband signal
outputted from said level adjusting means is adjusted to an
20 amplitude value matching a dynamic range of said D/A
converting means based on the number of transmission codes
which is the number of multiplexed baseband signals and,
for notifying said level adjusting means of the gain set
value with said control signal.

5. A baseband signal multiplexing circuit for
multiplexing a plurality of baseband signals spread with
different spread codes into one baseband signal,
comprising:

5 a plurality of baseband filters for respectively
limiting bands of the respective baseband signals input
thereto;

a plurality of first level adjusting means for
respectively adjusting amplitude values of the plurality of
10 baseband signals with the bands limited by said respective
baseband filters based on a plurality of first control
signals to output the signals;

adding means for adding and code-multiplexing

the plurality of baseband signals outputted from said
15 respective first level adjusting means to produce one
baseband signal;

second level adjusting means for adjusting an
amplitude value of the baseband signal produced by said
adding means based on a second control signal to output the
20 signal;

D/A converting means for converting the baseband
signal which is a digital signal outputted from said second
level adjusting into an analog signal; and

gain setting means for outputting to said
25 respective first level adjusting means the first control
signals for adjusting amplitude ratios of the respective
baseband signals in accordance with interchannel ratio
information for specifying amplitude ratios of the
respective baseband signals when the plurality of baseband
30 signals are multiplexed, for calculating a gain set value
with which the amplitude value of the baseband signal
outputted from said second level adjusting means is
adjusted to an amplitude value matching a dynamic range of
said D/A converting means based on the number of
35 transmission codes which is the number of multiplexed
baseband signals, and for notifying said second level
adjusting means of the gain set value with said second
control signal.

6. A baseband signal multiplexing circuit for multiplexing a plurality of baseband signals spread with different spread codes into one baseband signal, comprising:

5 a plurality of first level adjusting means for respectively adjusting amplitude values of said respective baseband signals input thereto based on a plurality of first control signals;

adding means for adding and code-multiplexing
10 the plurality of baseband signals outputted from said respective first level adjusting means to produce one baseband signal;

a baseband filter for limiting a band of the baseband signal produced by said adding means;

15 second level adjusting means for adjusting an amplitude value of the baseband signal with the band limited by said baseband filter based on a second control signal to output the signal;

D/A converting means for converting the baseband
20 signal which is a digital signal outputted from said second level adjusting means into an analog signal; and

gain setting means for outputting to said respective first level adjusting means the first control signals for adjusting an amplitude ratio of the respective

25 baseband signals in accordance with interchannel ratio
information for specifying an amplitude ratio of the
respective baseband signals when the plurality of baseband
signals are multiplexed, for calculating a gain set value
with which the amplitude value of the baseband signal
30 outputted from said second level adjusting means is
adjusted to an amplitude value matching a dynamic range of
said D/A converting means based on the number of
transmission codes which is the number of multiplexed
baseband signals, and for notifying said second level
35 adjusting means of the gain set value with said second
control signal.

7. A level adjusting circuit comprising:

a plurality of bit shift means for shifting
input baseband signals to the right by different certain
bits;

5 a plurality of switches for selecting outputs
from said respective bit shift means in accordance with a
desired gain desired to be set; and

an adder for adding outputs from said respective
switches for output as one signal.

8. A baseband signal multiplexing circuit for
multiplexing a plurality of baseband signals spread with

different spread codes into one baseband signal,
comprising:

5 a plurality of baseband filters for respectively
limiting bands of the respective baseband signals input
thereto, and for adjusting amplitude values of the
respective baseband signals based on a control signal to
output the signals;

10 adding means for adding and code-multiplexing
the plurality of baseband signals with the bands limited by
said respective baseband filters to produce one baseband
signal;

15 D/A converting means for converting the baseband
signal which is a digital signal outputted from said adding
means into an analog signal; and

20 gain setting means for calculating a gain set
value with which an amplitude value of the baseband signal
outputted from said adding means is adjusted to an
amplitude value matching a dynamic range of said D/A
converting means based on the number of transmission codes
which is the number of multiplexed baseband signals and for
notifying said level adjusting means of the gain set value
with said control signal.

9. The baseband signal multiplexing circuit
according to claim 8, wherein each of said respective

baseband filters includes:

5 a plurality of delay elements connected in series, for delaying input signals by a certain time period to output the signals as tap outputs;

a plurality of coefficient multipliers, for multiplying each of the tap outputs by a filter coefficient of a plurality of preset filter coefficients that is
10 specified by a control signal; and

an adder for adding a plurality of output signals from said respective coefficient multipliers to output the resulting signal.

10. A method of controlling a transmission level in a baseband signal multiplexing circuit for multiplexing a plurality of baseband signals spread with different spread codes into one baseband signal, said method comprising the
5 steps of:

calculating a gain set value with which an amplitude value of a multiplexed baseband signal matches a dynamic range in D/A conversion based on the number of transmission codes which is the number of multiplexed
10 baseband signals; and

adjusting the amplitude value of the code-multiplexed baseband signal prior to the D/A conversion based on the gain set value.

11. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread
codes into one baseband signal, said method comprising the
5 steps of:

limiting bands of the respective baseband
signals input thereto;

adding and code-multiplexing the plurality of
baseband signals with the limited bands to produce one
10 baseband signal;

calculating a gain set value with which an
amplitude value of the code-multiplexed baseband signal
matches a dynamic range in D/A conversion based on the
number of transmission codes which is the number of
15 multiplexed baseband signals;

adjusting the amplitude value of the code-
multiplexed baseband signal based on the gain set value;
and

D/A converting the baseband signal with the
20 adjusted amplitude value into an analog signal.

12. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread

codes into one baseband signal, said method comprising the

5 steps of:

limiting bands of the respective baseband
signals input thereto;

calculating a gain set value with which
amplitude values of the plurality of baseband signals with
10 the limited bands match a dynamic range in D/A conversion
based on the number of transmission codes which is the
number of multiplexed baseband signals;

adjusting the amplitude values of the plurality
of baseband signals with the limited bands based on the
15 gain set value;

adding and code-multiplexing the plurality of
baseband signals after the adjustment of the amplitude
values to produce one baseband signal; and

D/A converting the baseband signal after the
20 code-multiplexing into an analog signal.

13. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread
codes into one baseband signal, said method comprising the
5 steps of:

limiting bands of the respective baseband
signals input thereto;

calculating, for the respective baseband signals,
gain set values with which amplitude values of the
10 plurality of baseband signals with the limited bands match
a dynamic range in D/A conversion based on the number of
transmission codes which is the number of multiplexed
baseband signals;

adjusting the amplitude values of the plurality
15 of baseband signals with the limited bands based on the
gain set values;

adding and code-multiplexing the plurality of
baseband signals after the adjustment of the amplitude
values to produce one baseband signal; and

20 D/A converting the baseband signal after the
code-multiplexing into an analog signal.

14. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread
codes into one baseband signal, said method comprising the
5 steps of:

adding and code-multiplexing the respective
baseband signals input thereto to produce one baseband
signal;

limiting a band of the code-multiplexed baseband
10 signal;

calculating a gain set value with which an
amplitude value of the baseband signal with the limited
band matches a dynamic range in D/A conversion based on the
number of transmission codes which is the number of
15 multiplexed baseband signals;

adjusting the amplitude value of the baseband
signal with the limited band based on the gain set value;
and

D/A converting the baseband signal after the
20 adjustment of the amplitude value into an analog signal.

15. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread
codes into one baseband signal, said method comprising the
5 steps of:

limiting bands of the respective baseband
signals input thereto;

adjusting respective amplitude values of the
plurality of baseband signals with the limited bands based
10 on a specified ratio;

adding and code-multiplexing the respective
baseband signals after the adjustment of the amplitude
values to produce one baseband signal;

calculating a gain set value with which an

15 amplitude value of the code-multiplexed baseband signal
matches a dynamic range in D/A conversion based on the
number of transmission codes which is the number of
multiplexed baseband signals;

adjusting the amplitude value of the code-
20 multiplexed baseband signal based on the gain set value;
and

D/A converting the baseband signal after the
adjustment of the amplitude value based on the gain set
value into an analog signal.

16. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread
codes into one baseband signal, said method comprising the
5 steps of:

adjusting respective amplitude values of the
respective baseband signals input thereto based on a
specified ratio;

adding and code-multiplexing the respective
10 baseband signals after the adjustment of the amplitude
values to produce one baseband signal;

limiting a band of the code-multiplexed baseband
signal;

calculating a gain set value with which an

15 amplitude value of the baseband signal with the limited
band matches a dynamic range in D/A conversion based on the
number of transmission codes which is the number of
multiplexed baseband signals;

adjusting the amplitude value of the baseband
20 signal with the limited band based on the gain set value;
and

D/A converting the baseband signal after the
adjustment of the amplitude value based on the gain set
value into an analog signal.

17. A method of controlling a transmission level in
a baseband signal multiplexing circuit for multiplexing a
plurality of baseband signals spread with different spread
codes into one baseband signal, said method comprising the
5 steps of:

calculating a gain set value with which
amplitude values of the respective baseband signals input
thereto match a dynamic range in D/A conversion based on
the number of transmission codes which is the number of
10 multiplexed baseband signals;

limiting bands of the input respective baseband
signals, and adjusting the amplitude values of the
respective baseband signals based on the gain set value by
selecting a filter coefficient to be multiplied by each of

15 tap outputs obtained by delaying the input baseband signals
by a certain time period;

adding and code-multiplexing the plurality of
baseband signals with the limited bands and the adjusted
amplitude values to produce one baseband signal; and

20 converting the code-multiplexed baseband signal
which is a digital signal into an analog signal.

ABSTRACT OF THE DISCLOSURE

A baseband signal multiplexing circuit for controlling an amplitude level of a baseband signal to be transmitted on the basis of the number of transmission
5 codes is disclosed. Gain setting means calculates a gain set value with which an amplitude value of the baseband signal outputted from level adjusting means is adjusted to an amplitude value matching a dynamic range of D/A
10 converting means based on transmission code number information transmitted from a signal processor, and notifies the level adjusting means of the gain set value with a control signal. The level adjusting means controls the amplitude level of the baseband signal to be transmitted with the control signal. Therefore, it is
15 possible to effectively utilize the dynamic range of the D/A converting means independently of the number of transmission codes.

Fig. 1

PRIOR ART

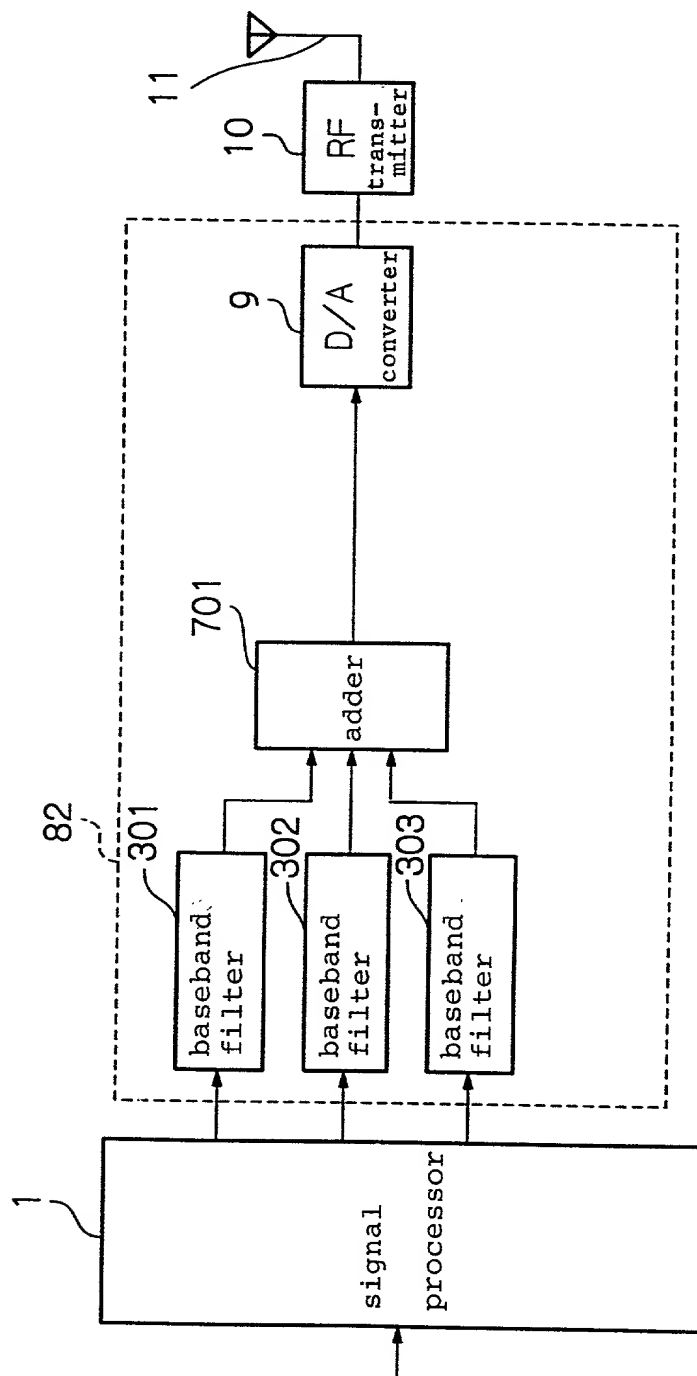


Fig. 2

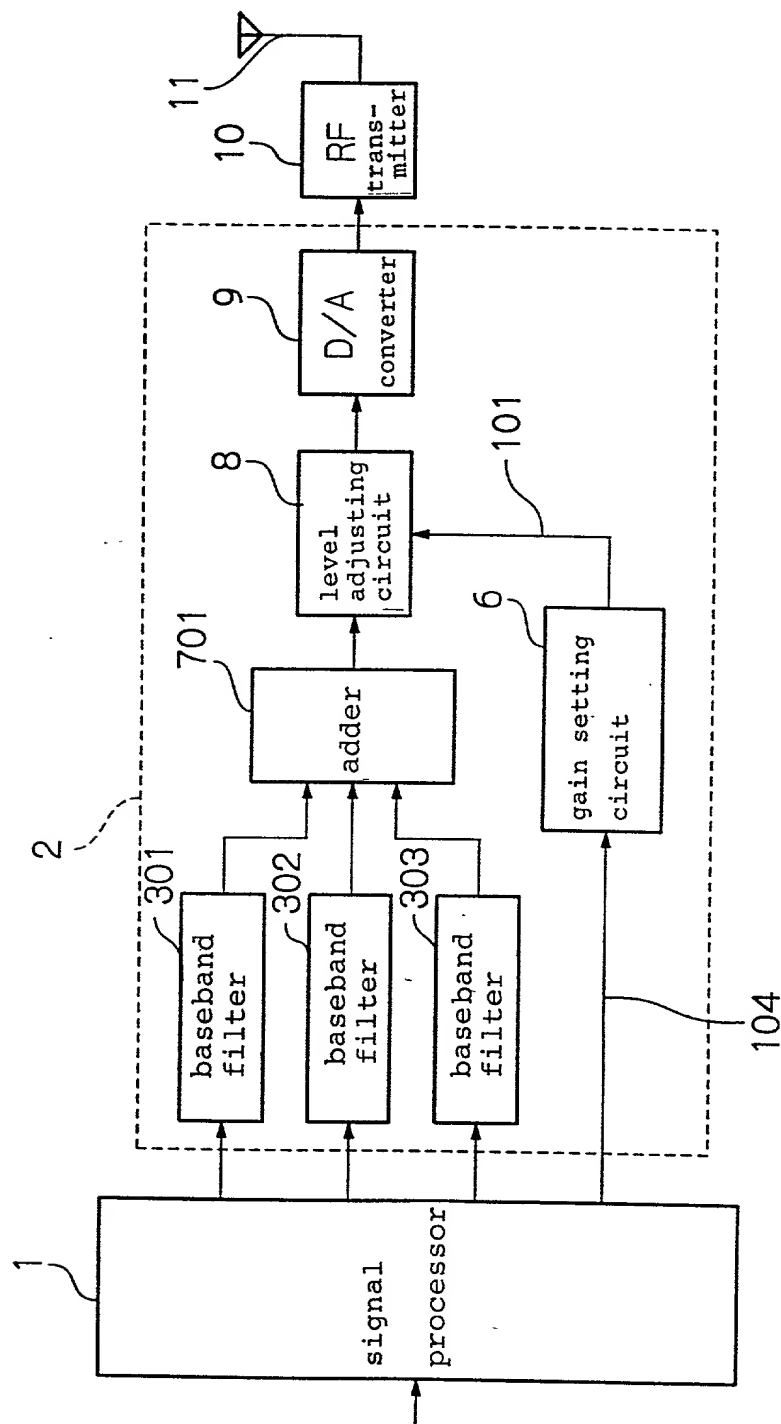
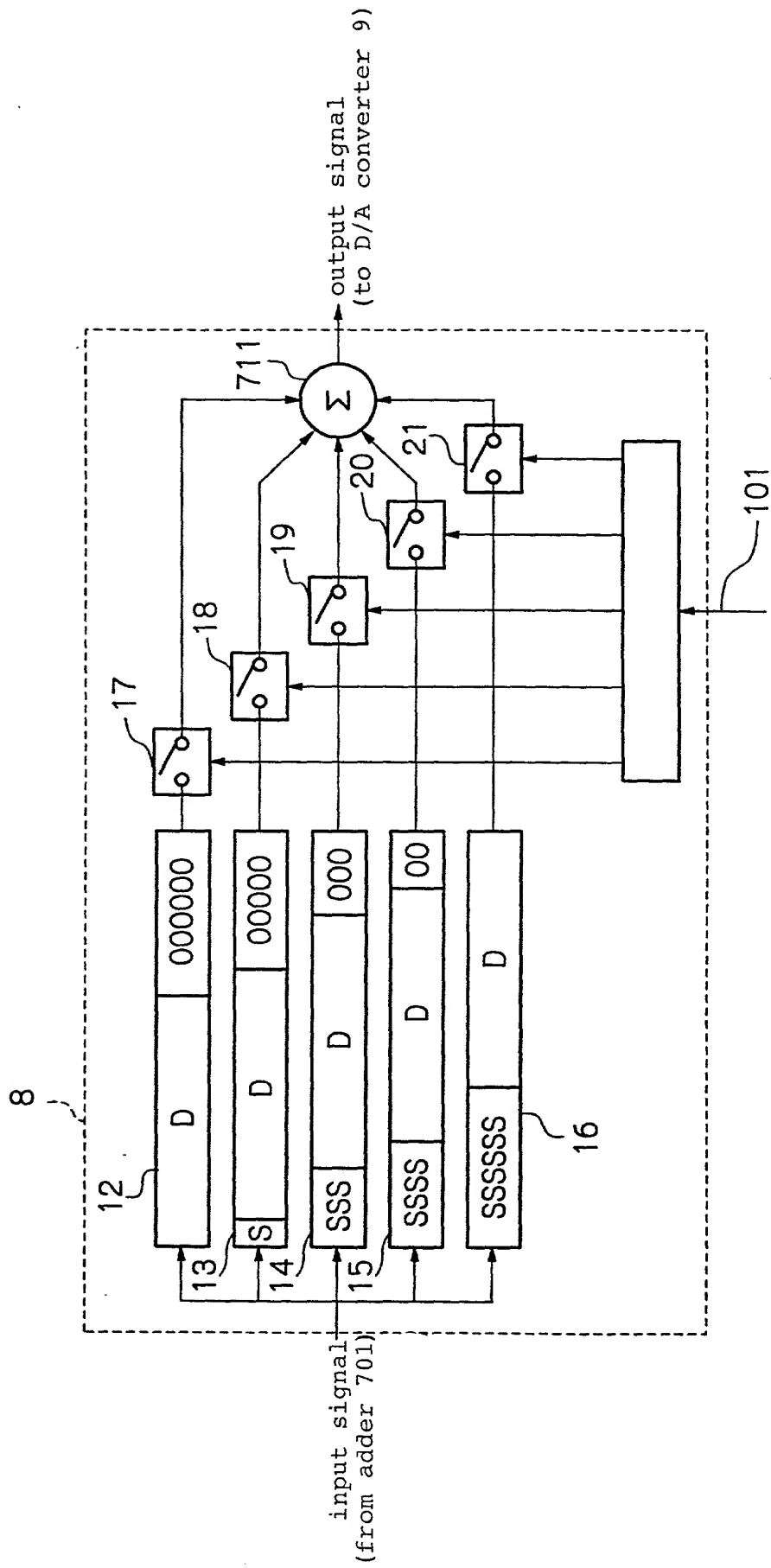


Fig. 3



F i g. 4 a

$$0 . \begin{array}{cccccccc} 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ \frac{1}{2} & \frac{1}{4} & \frac{1}{8} & \frac{1}{16} & \frac{1}{32} & \frac{1}{64} & \frac{1}{128} & \frac{1}{256} \end{array}$$

F i g. 4 b

$$\frac{1}{\sqrt{2}} = 0.7071 \dots \div \frac{1}{2} + \frac{1}{8} + \frac{1}{16} + \frac{1}{64}$$

$$\frac{1}{\sqrt{2}} = (\text{decimal number}) = 0 . \begin{array}{cccccccc} 1 & 0 & 1 & 1 & 0 & 1 & 0 & 0 \\ \vdots & & \vdots & \vdots & & \vdots & & \\ \frac{1}{2} & & \frac{1}{8} & \frac{1}{16} & & \frac{1}{64} & & \end{array} \begin{array}{l} \text{(binary} \\ \text{number)} \end{array}$$

F i g. 4 c

$$\frac{1}{\sqrt{3}} = 0.5773 \dots \div \frac{1}{2} + \frac{1}{16} + \frac{1}{64}$$

$$\frac{1}{\sqrt{3}} = (\text{decimal number}) = 0 . \begin{array}{ccccccc} 1 & 0 & 0 & 1 & 0 & 1 & 0 & 0 \\ \vdots & & & \vdots & & \vdots & & \\ \frac{1}{2} & & & \frac{1}{16} & & \frac{1}{64} & & \end{array} \begin{array}{l} \text{(binary} \\ \text{number)} \end{array}$$

Fig. 5

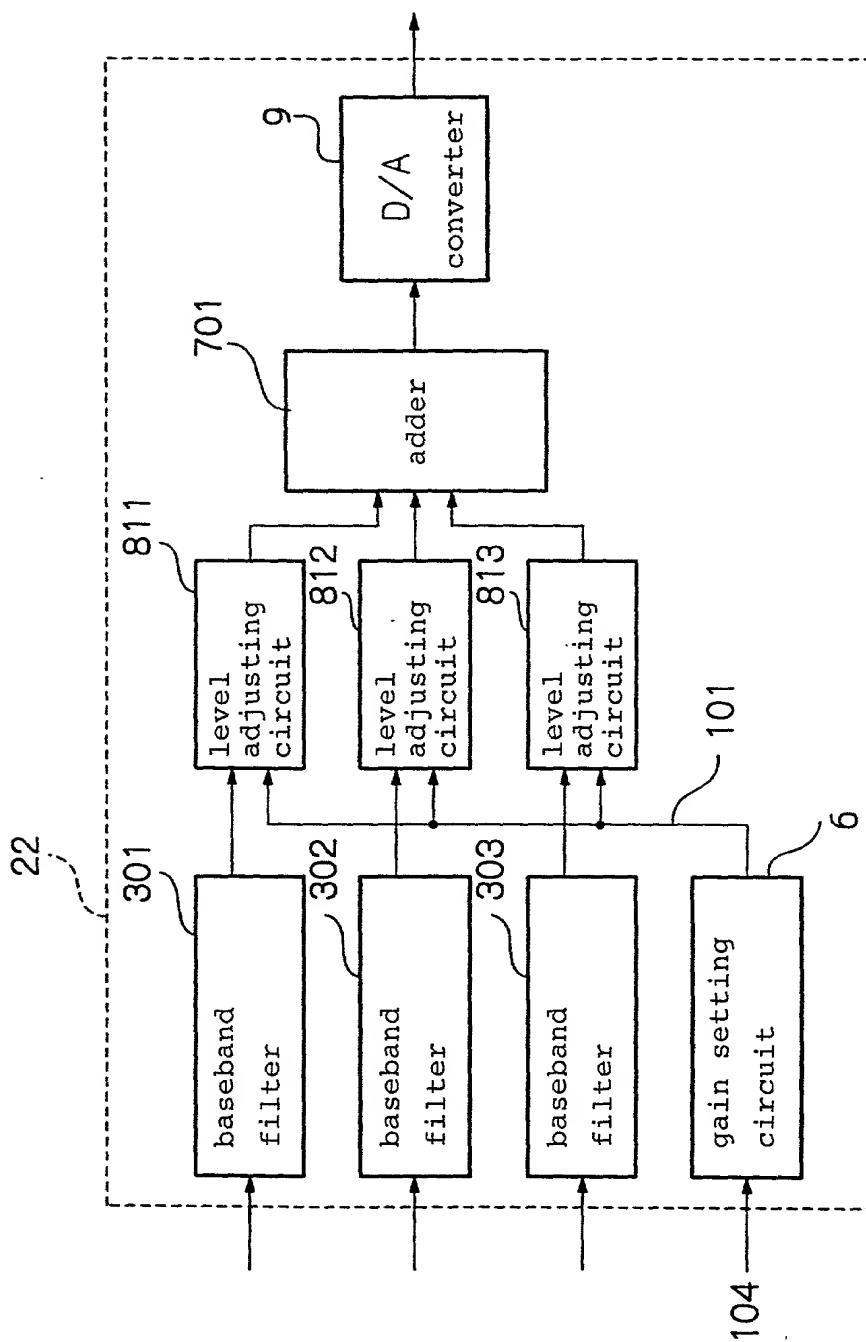


Fig. 6

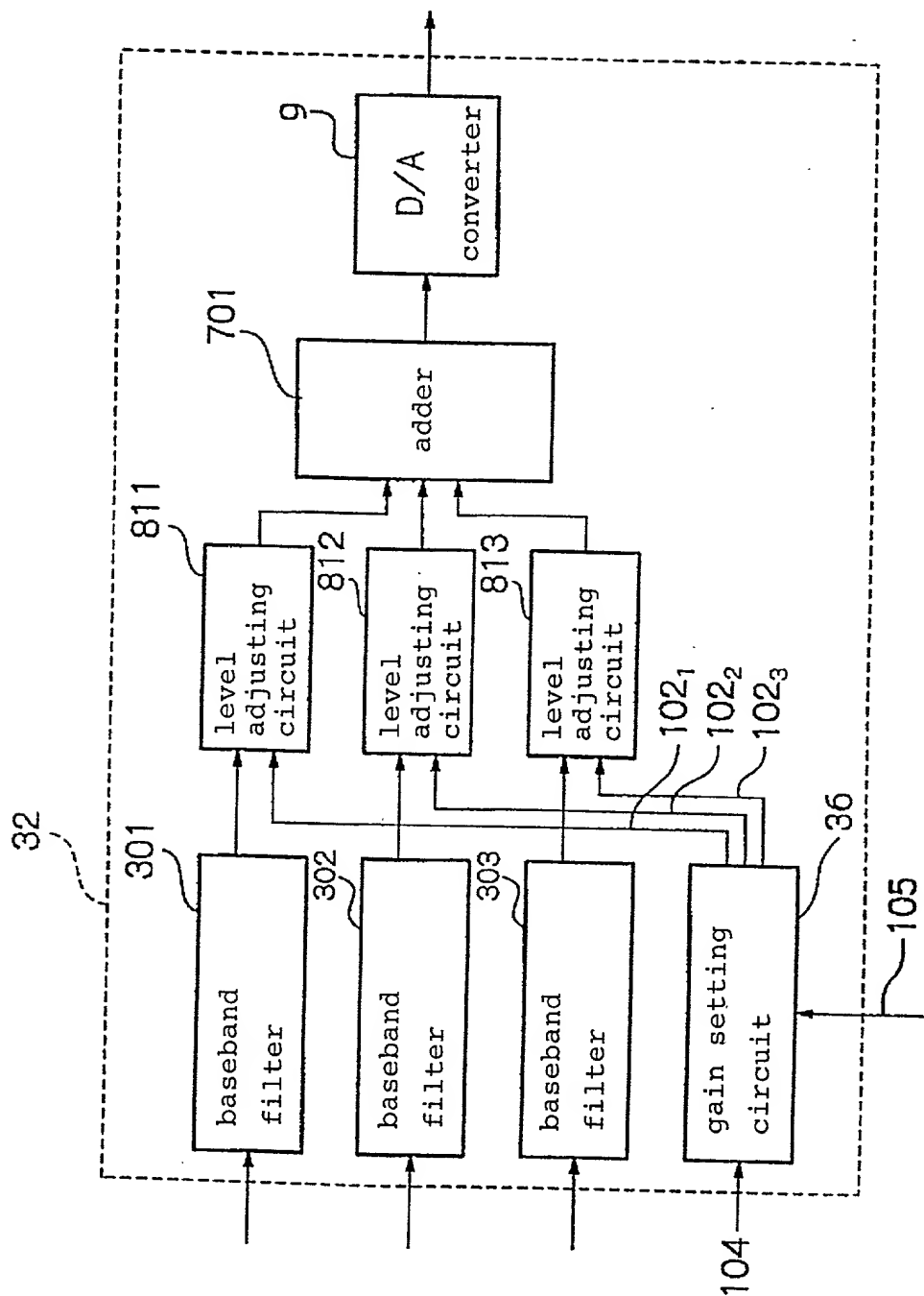


Fig. 7

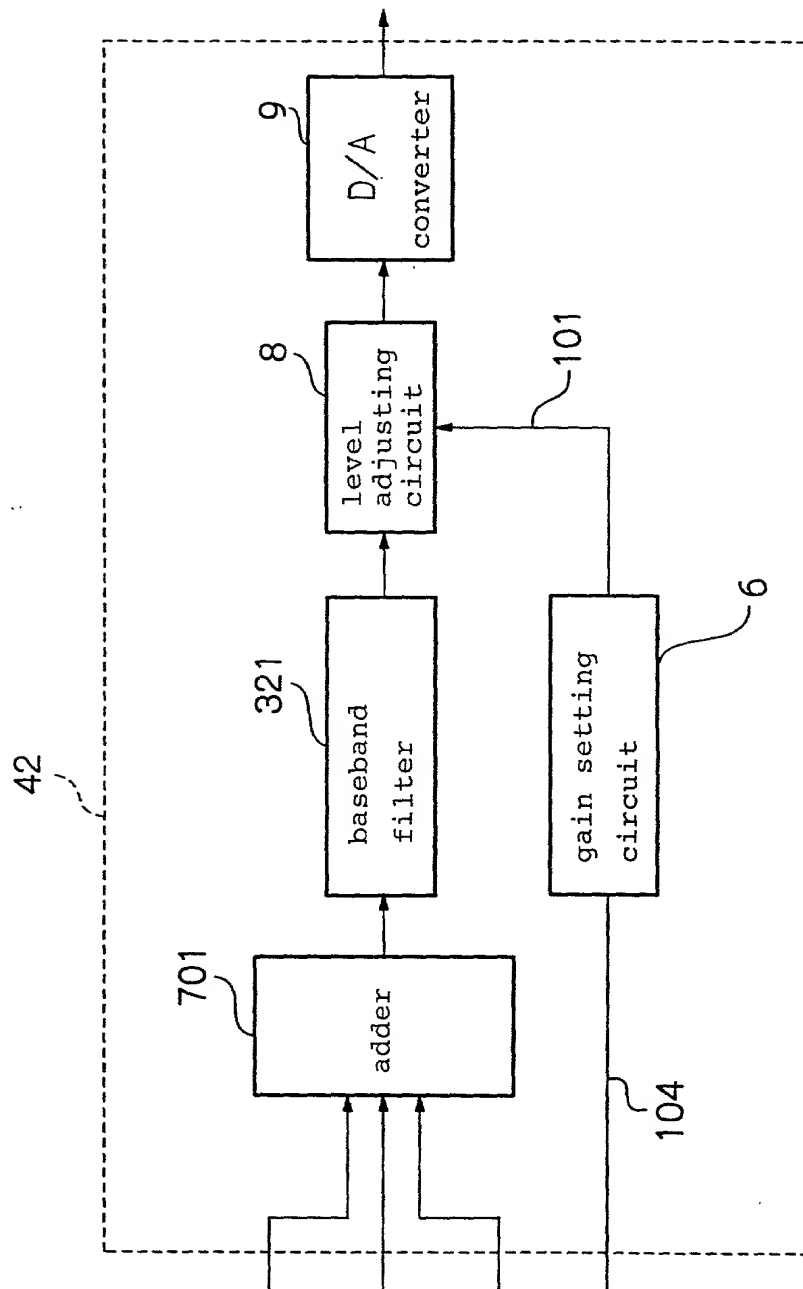


Fig. 8

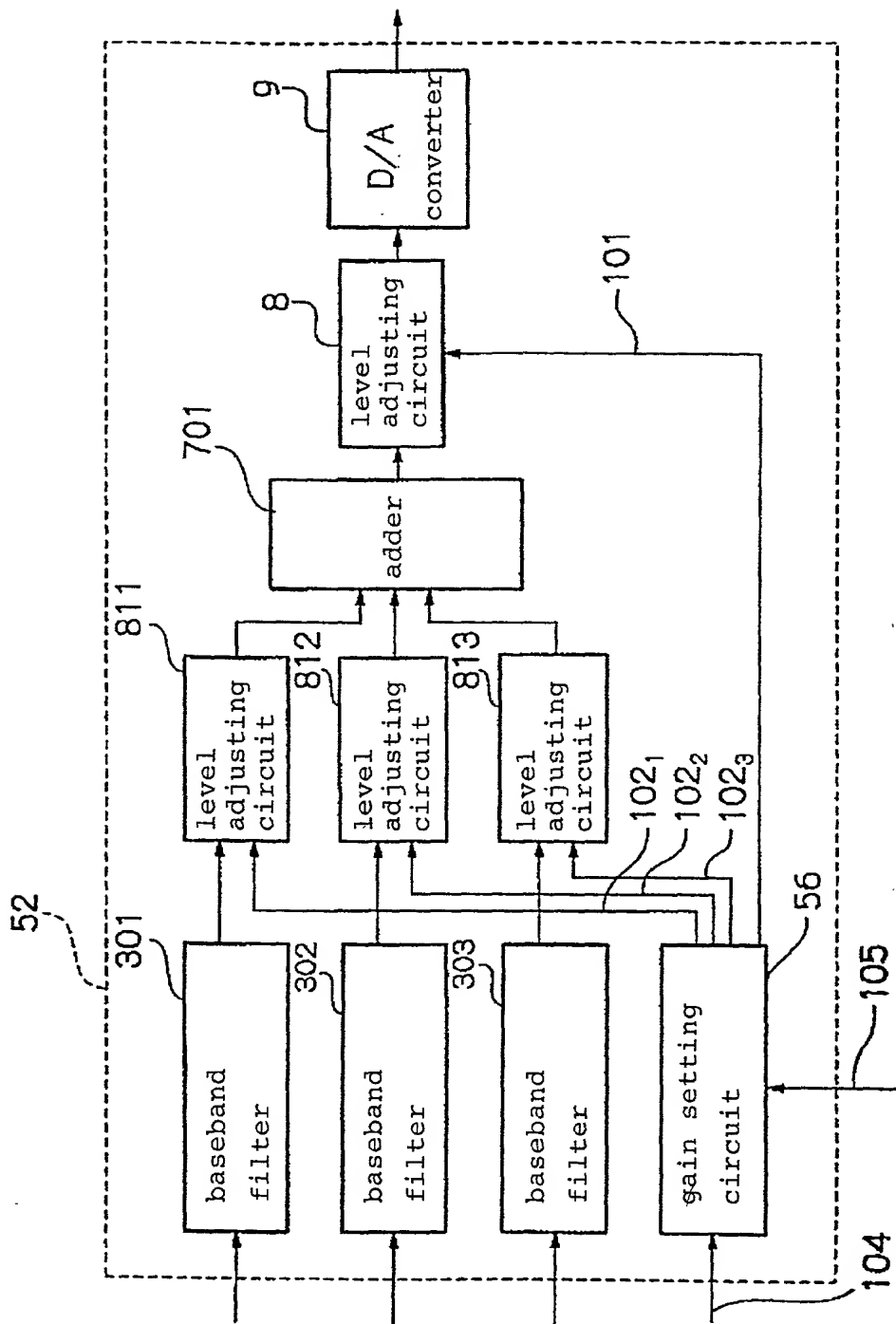


Fig. 9

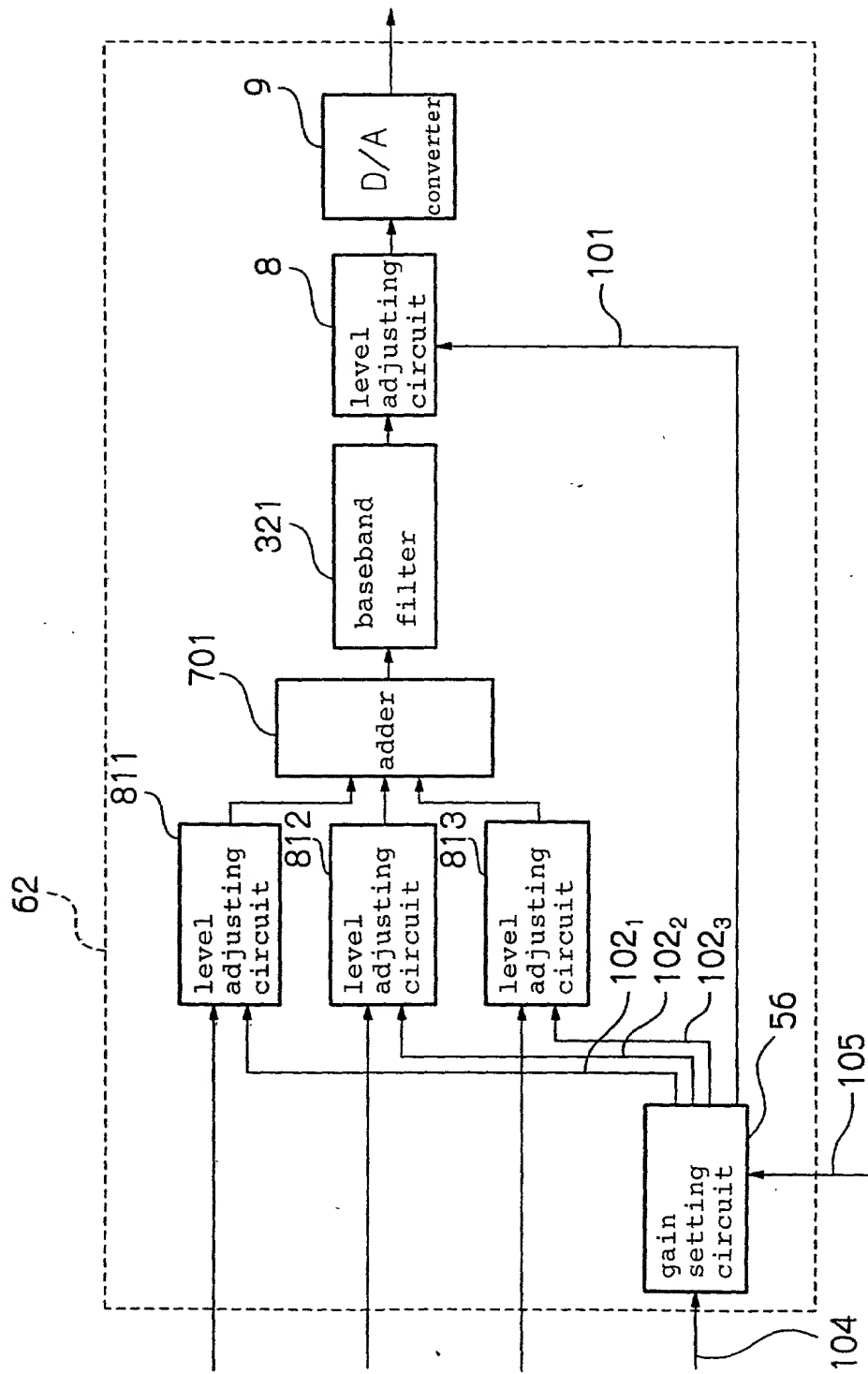


Fig. 10

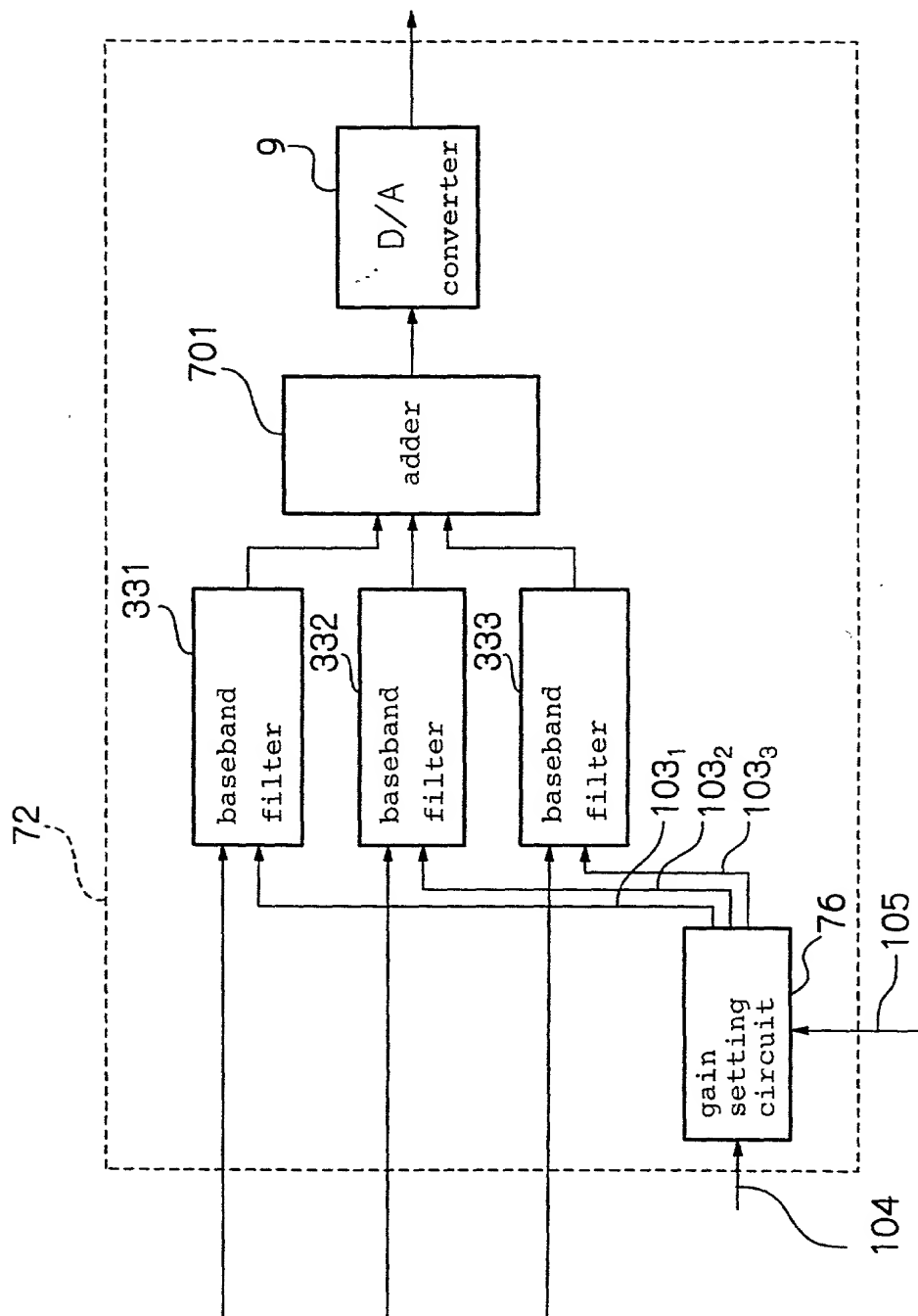
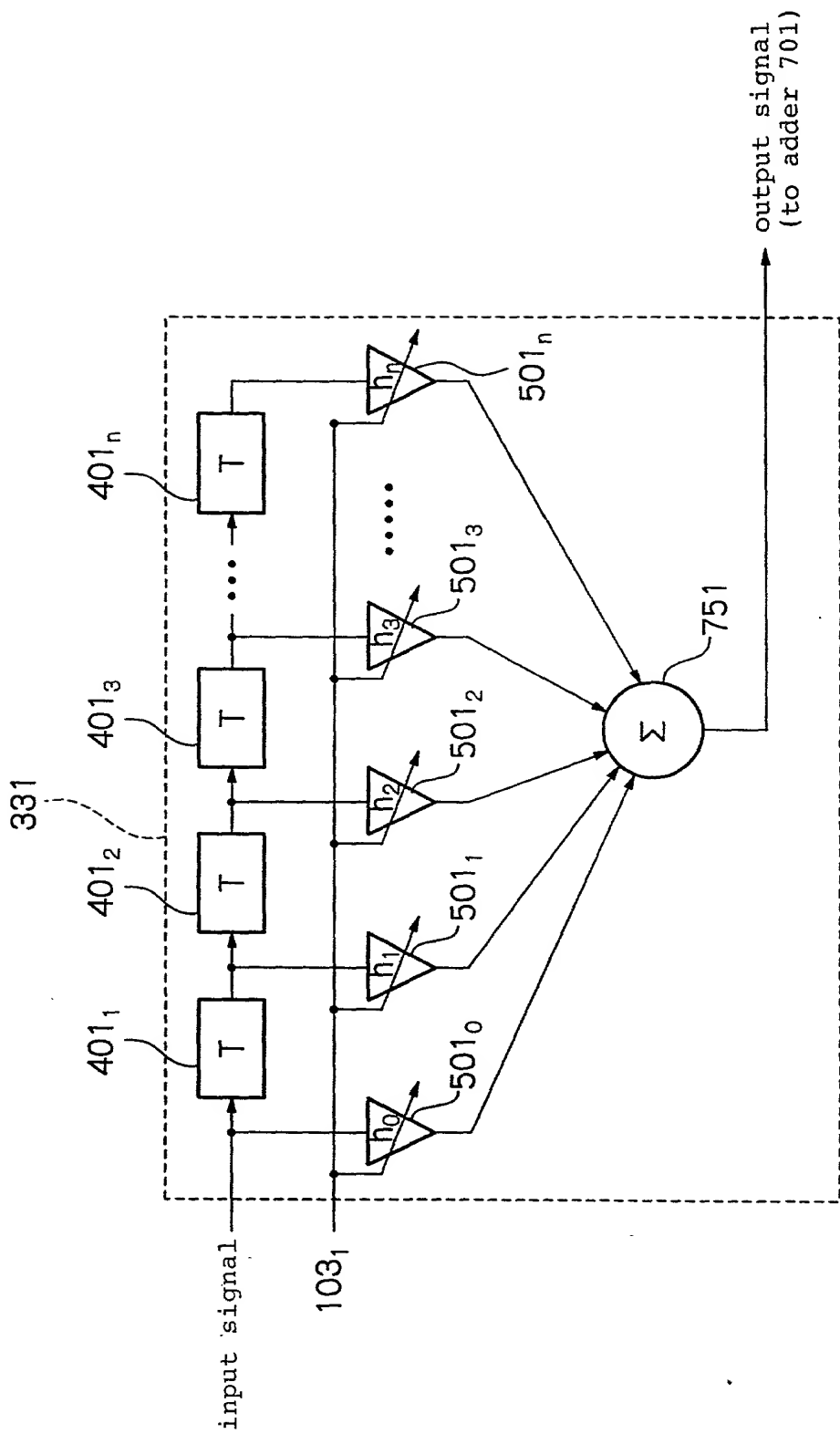


Fig. 11



UNITED STATES OF AMERICA
COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

QFGS FILE NO.

P 1878-135

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**CONTROL OF AMPLITUDE LEVEL OF BASEBAND SIGNAL TO BE TRANSMITTED ON
THE BASIS OF THE NUMBER OF TRANSMISSION CODES**

the specification of which is attached hereto, unless the following box is checked:

☐ was filed on _____ as United States patent Application Number or PCT International patent Number _____ and was amended on _____ (if any).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information known to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate or United States provisional application(s) listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign or Provisional Application(s)

COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 U.S.C. 119
Japan	359586/1998	17/12/1998	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
			YES <input type="checkbox"/> NO <input type="checkbox"/>
			YES <input type="checkbox"/> NO <input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

UNITED STATES APPLICATION NUMBER	DATE OF FILING (day, month, year)	STATUS (patented, pending, abandoned)

I hereby appoint OSTROLENK, FABER, GERB & SOFFEN, and the members of the firm, Marvin C. Soffen - Reg. No. 17,542; Samuel H. Weiner - Reg. No. 18,510; Jerome M. Berliner - Reg. No. 18,653; Robert C. Faber - Reg. No. 24,322; Edward A. Meilman - Reg. No. 24,735; Stanley H. Lieberstein - Reg. No. 22,400; Steven I. Weisburd - Reg. No. 27,409; Max Moskowitz - Reg. No. 30,576; Stephen A. Soffen - Reg. No. 31,063; James A. Finder - Reg. No. 30,173; William O. Gray, III - Reg. No. 30,944 and Louis C. Dujmich - Reg. No. 30,625, as attorneys with full power of substitution and revocation to prosecute this application, to transact all business in the Patent & Trademark Office connected therewith and to receive all correspondence.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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RESIDENCE		COUNTRY OF CITIZENSHIP	
POST OFFICE ADDRESS			
FULL NAME OF THIRD JOINT INVENTOR (IF ANY)		INVENTOR'S SIGNATURE	DATE
RESIDENCE		COUNTRY OF CITIZENSHIP	
POST OFFICE ADDRESS			